

PG161-A00

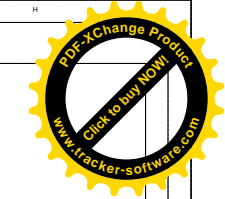
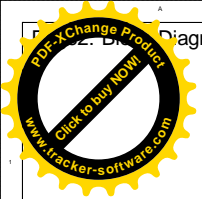
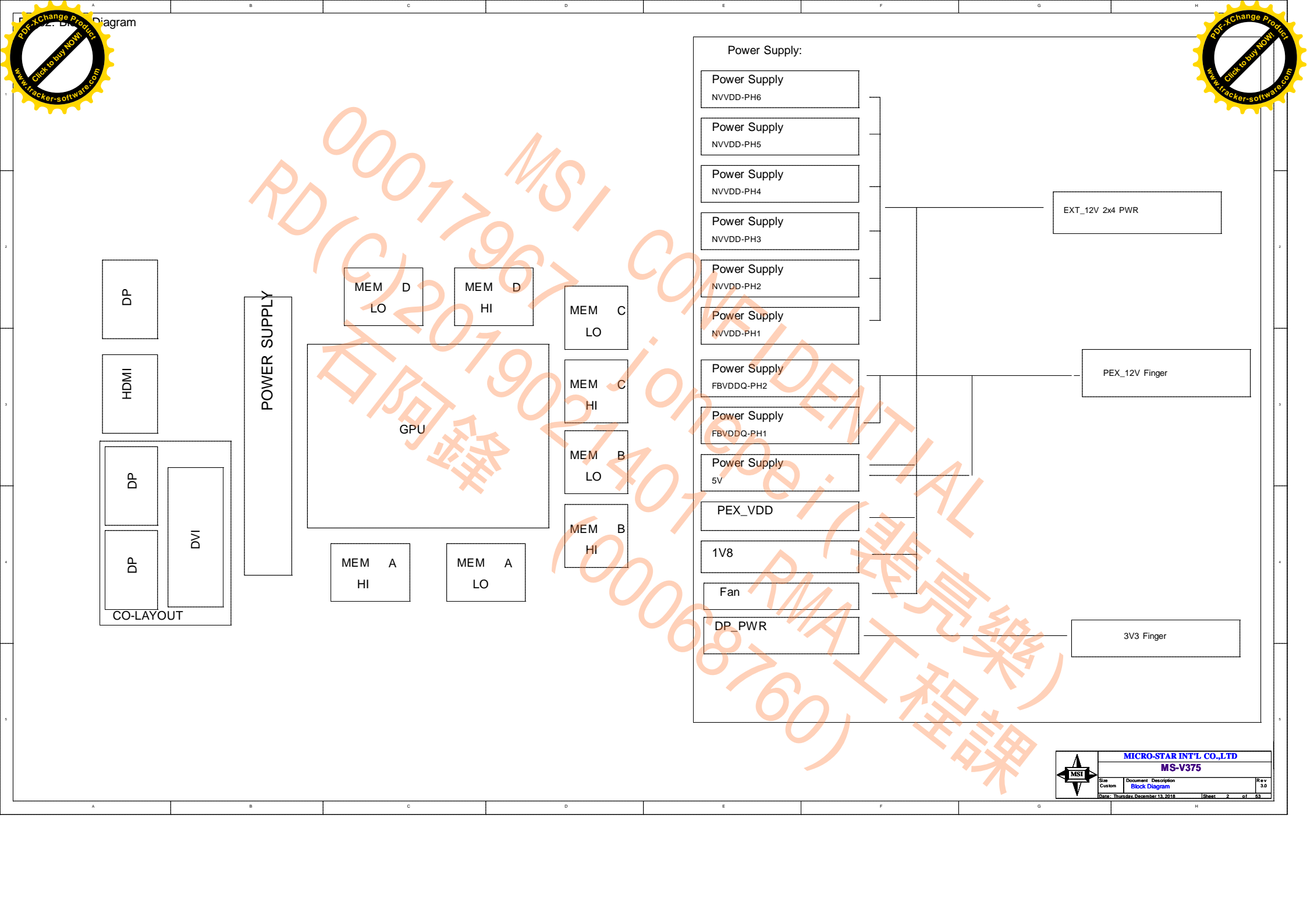
TU106 6GB GDDR6, 192b, X16
DVI-D/DP + DP + HDMI

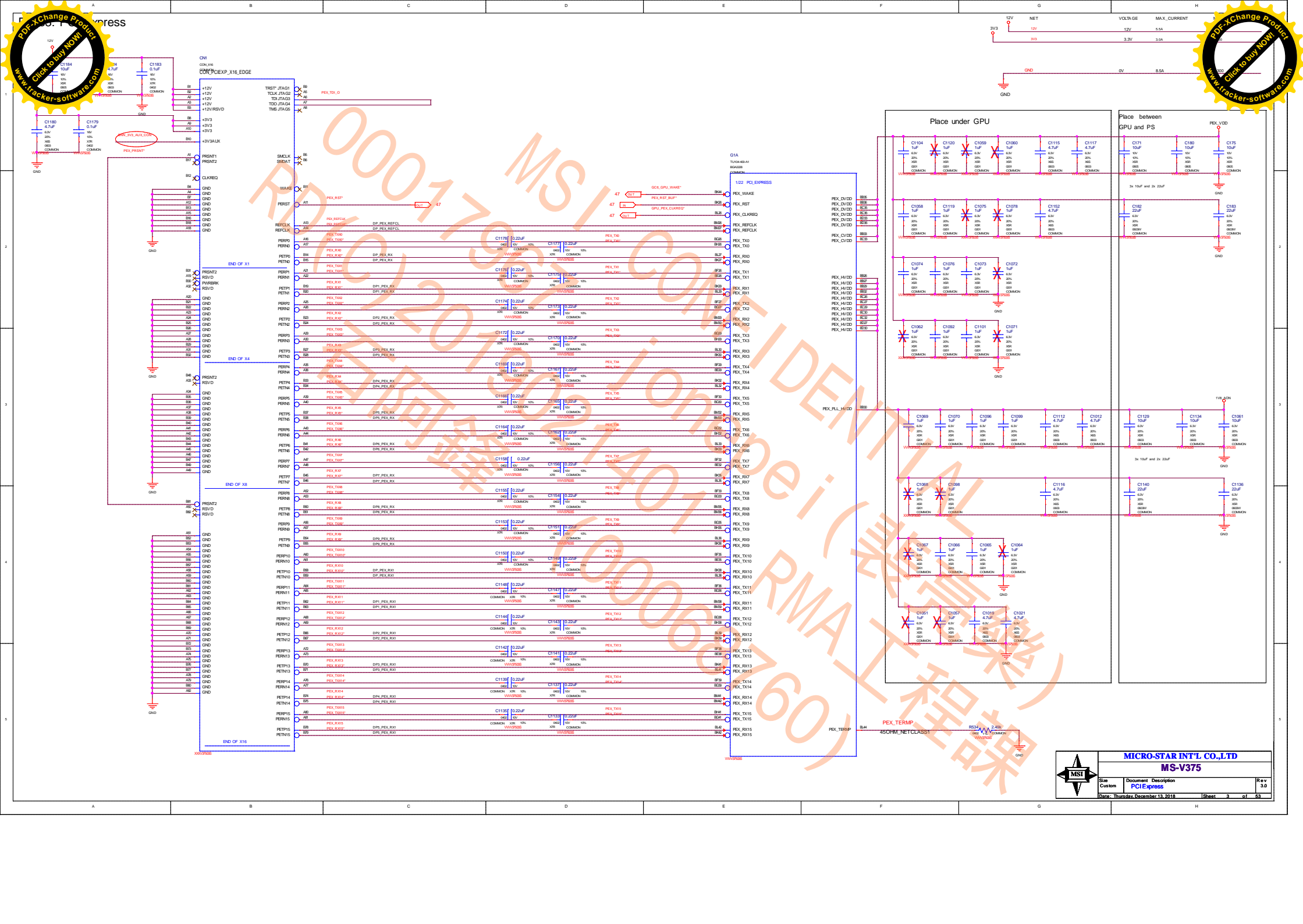
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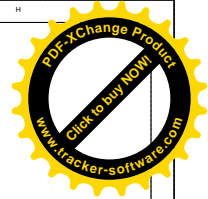
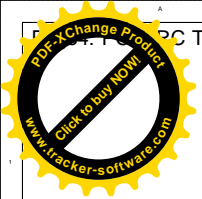
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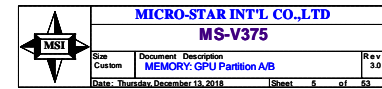




NO PEX GEN4 SUPPORT DELETE RC TERMINATIONS

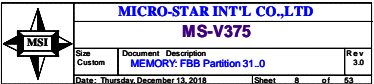
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RD(2)2019021401 jonepei (裴亮樂)
石阿鋒 (00068760) RMA工程課

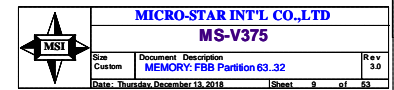
MICRO-STAR INT'L CO.,LTD			
MS-V375			
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Custom	PCE	RC TERM	3.0
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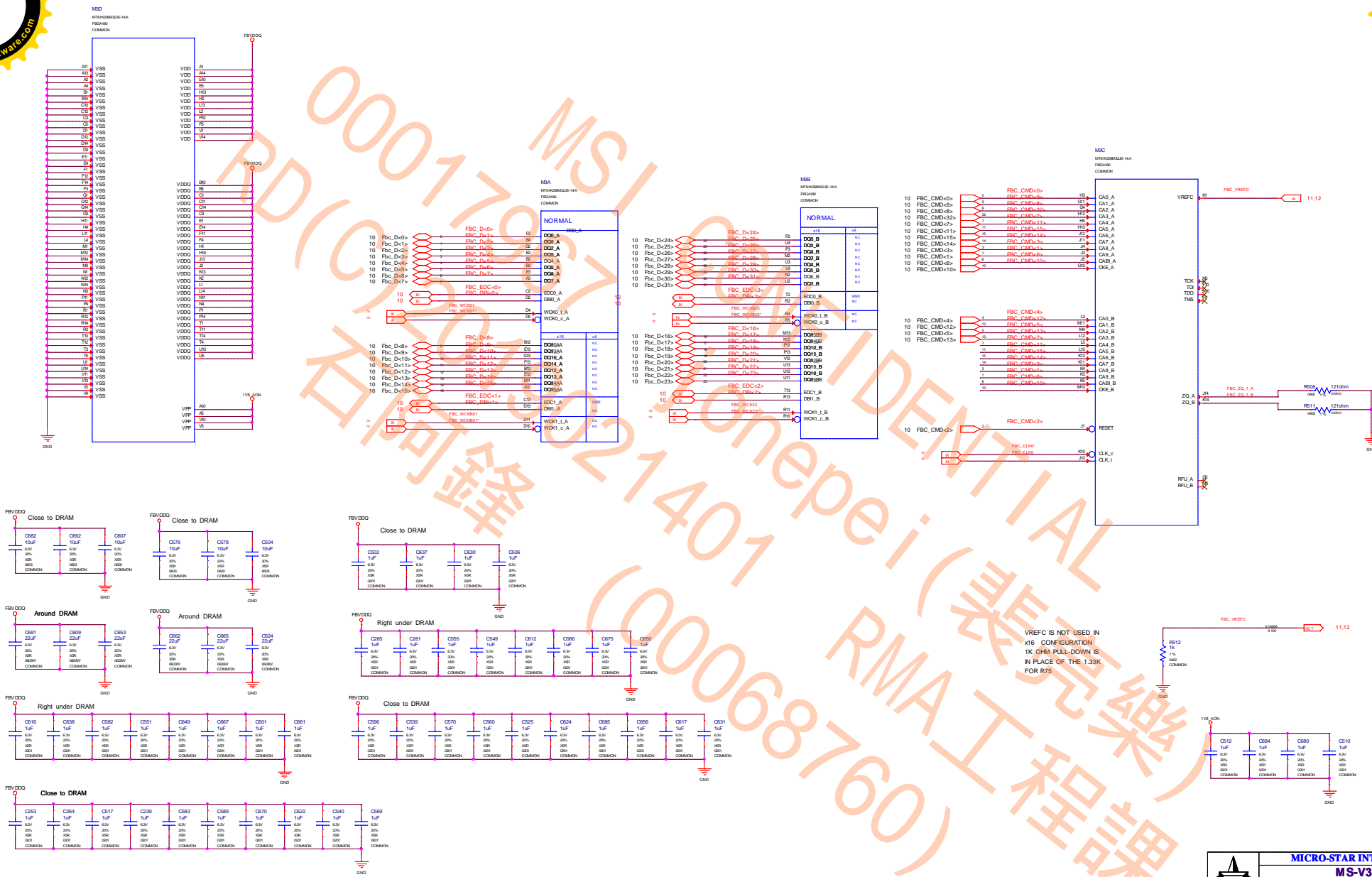


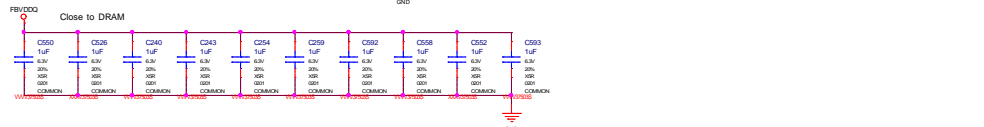
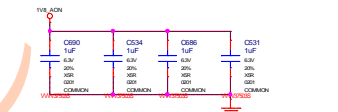
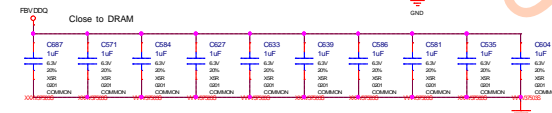
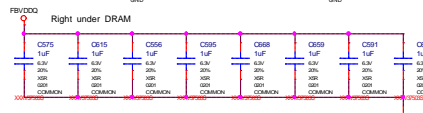
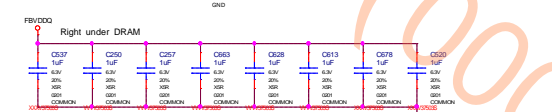
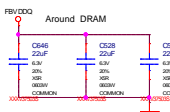
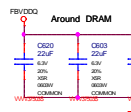
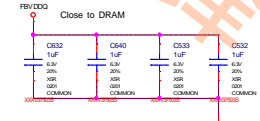
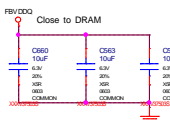
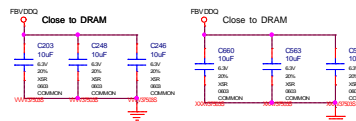
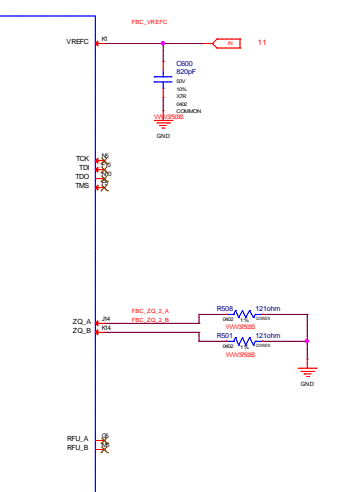
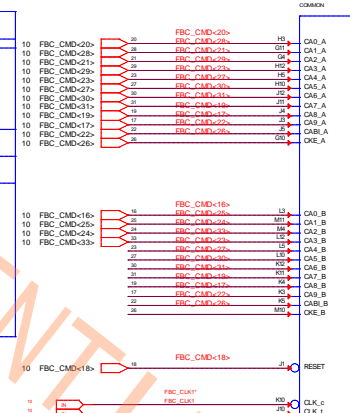
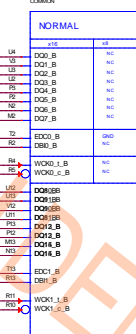
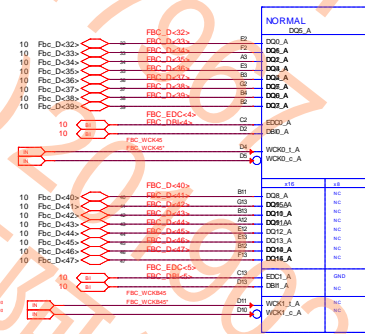
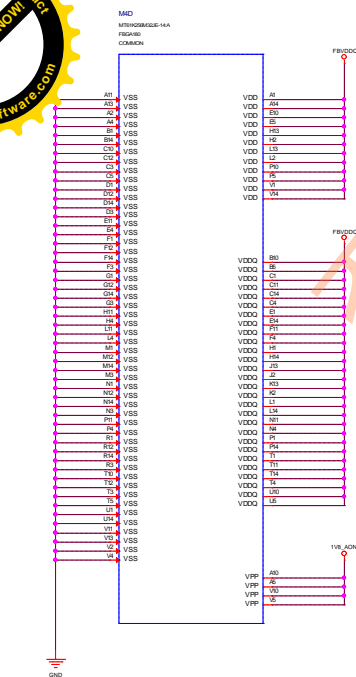


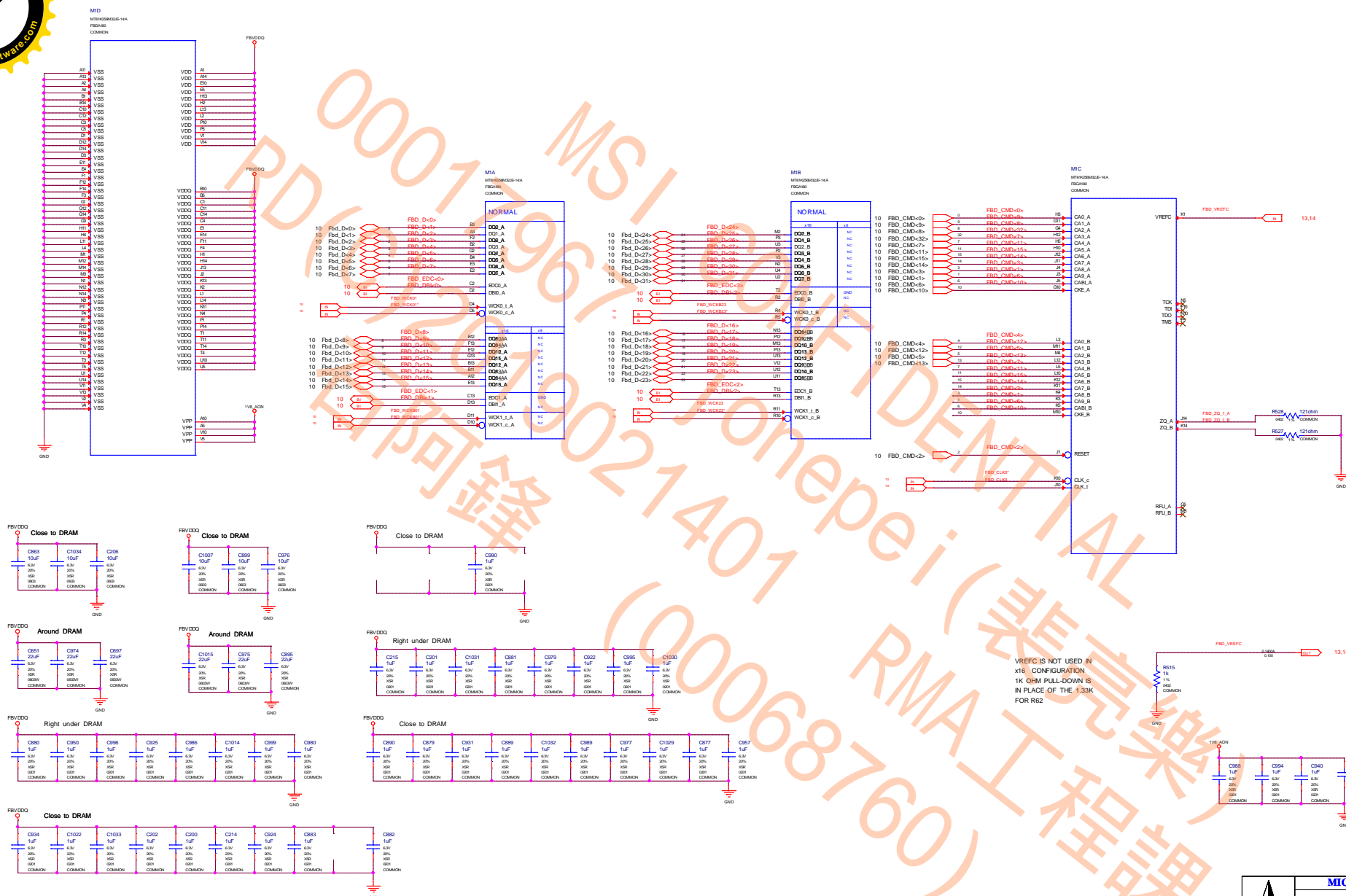
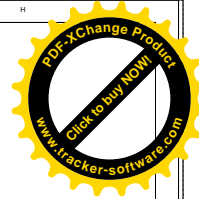


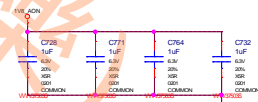
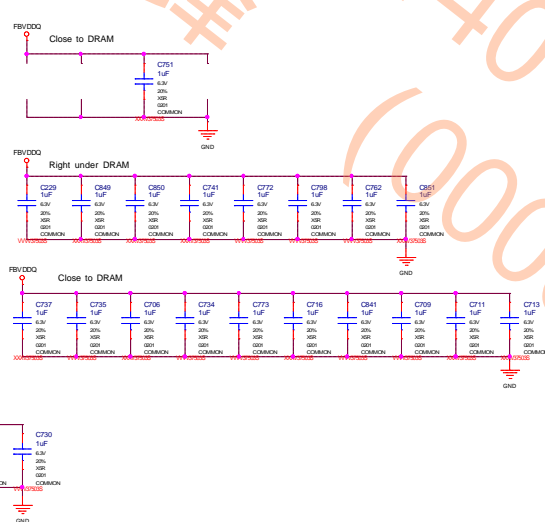
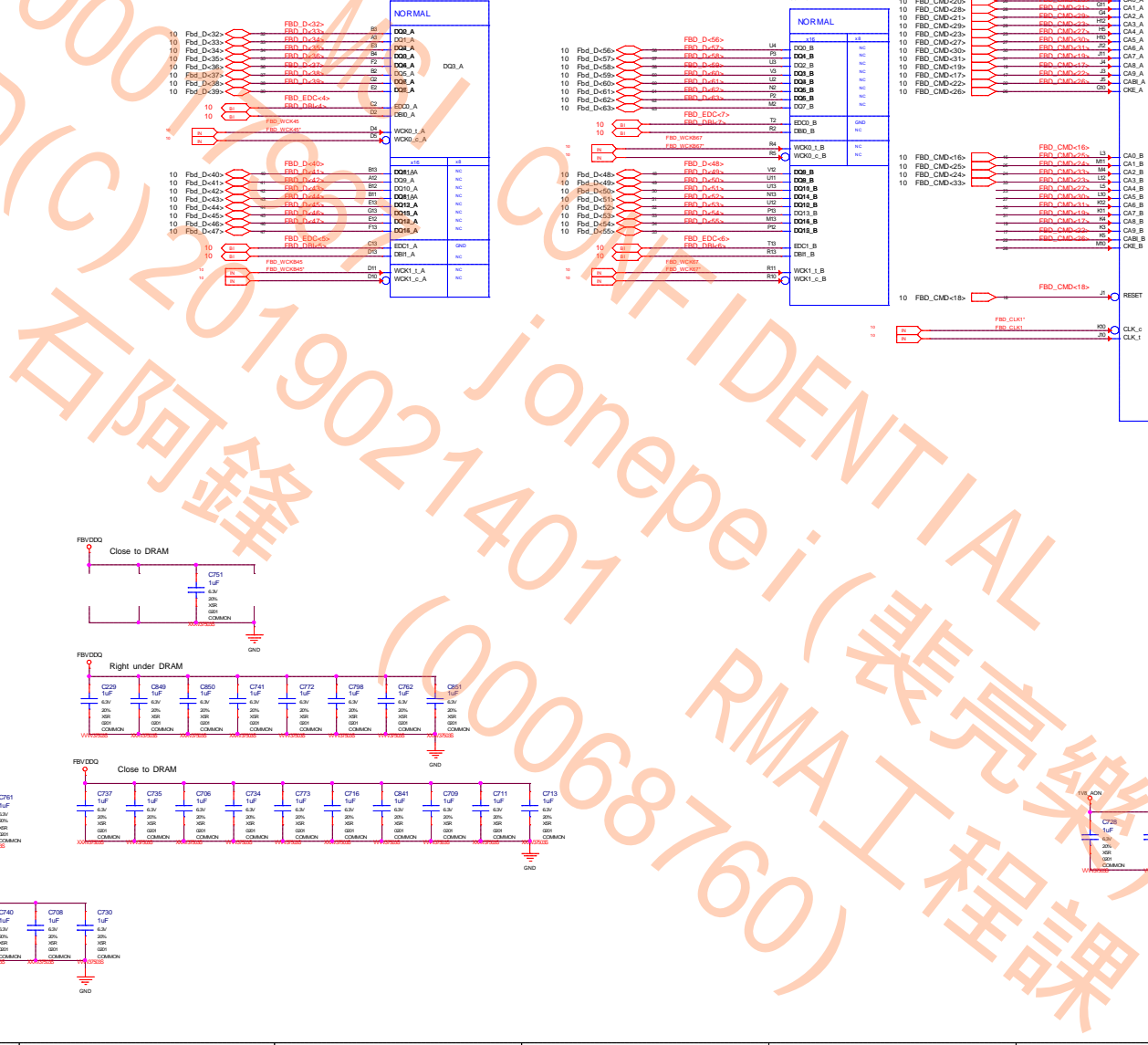
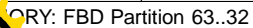


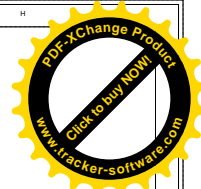


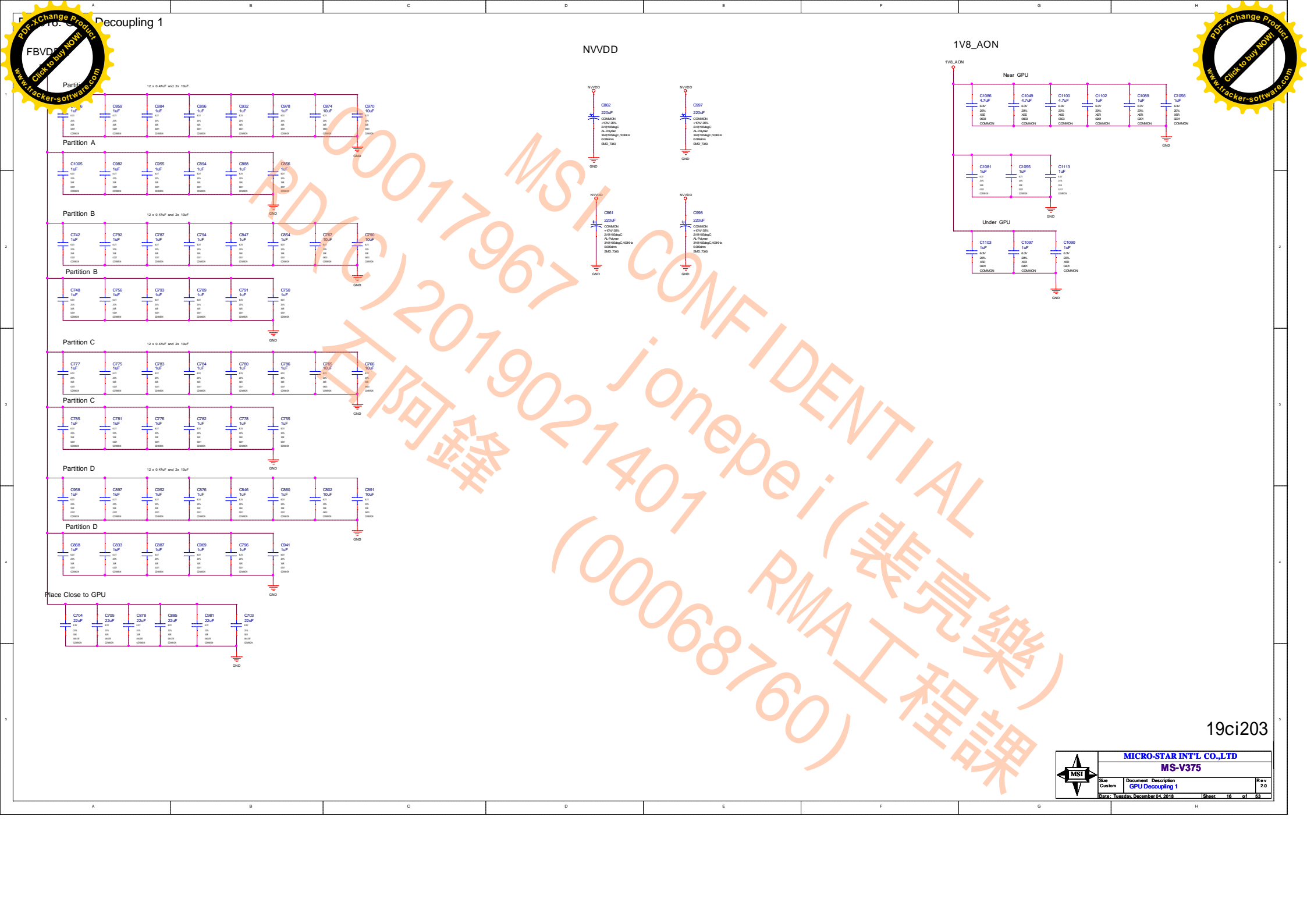


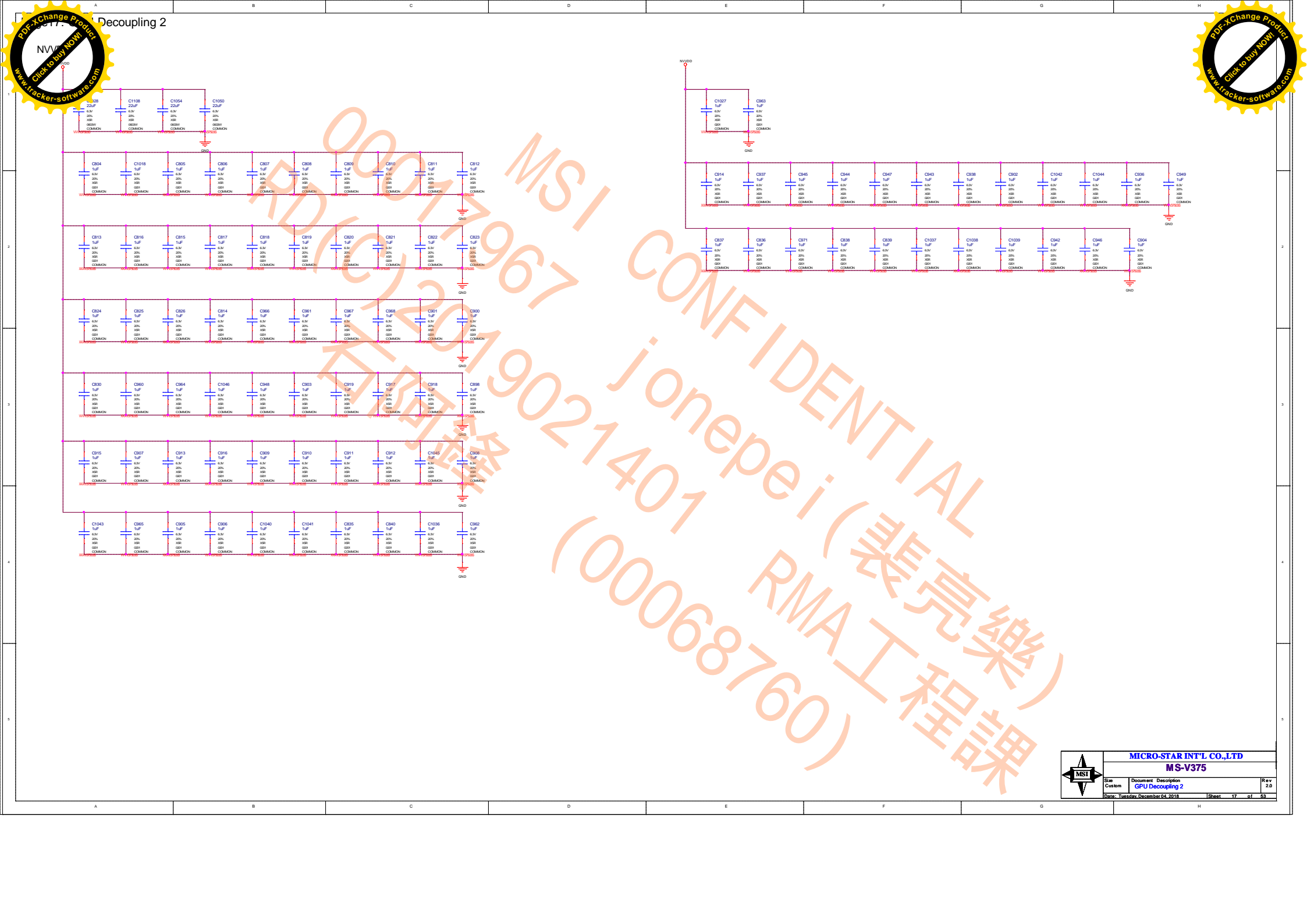


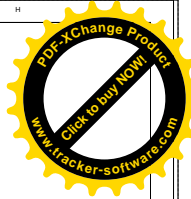
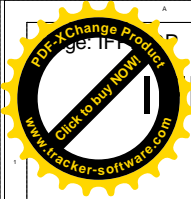






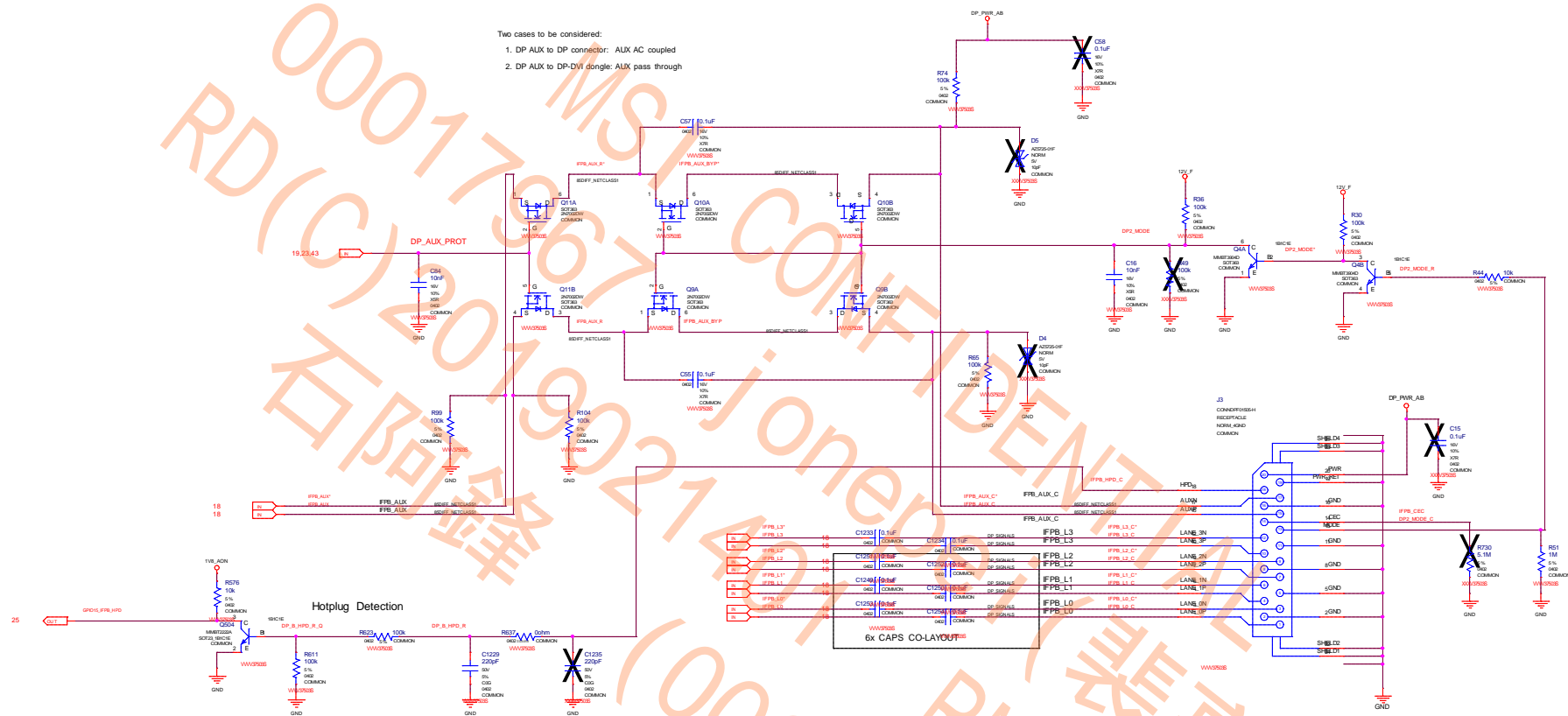




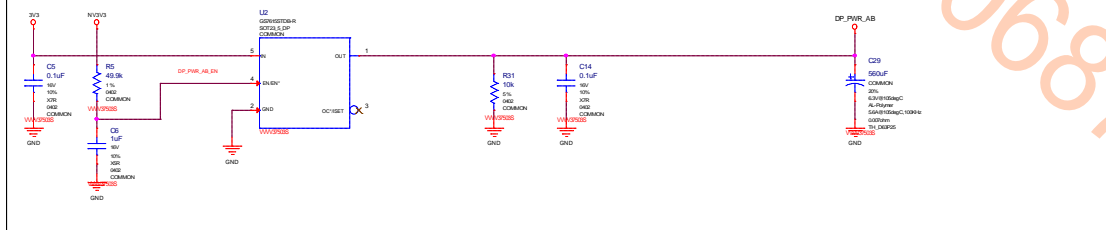


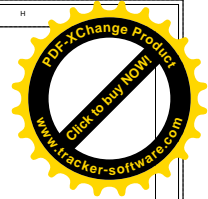
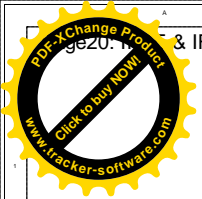
IFPB DP CO-LAYOUT WITH IFPAB DVI-D-DL

- Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through

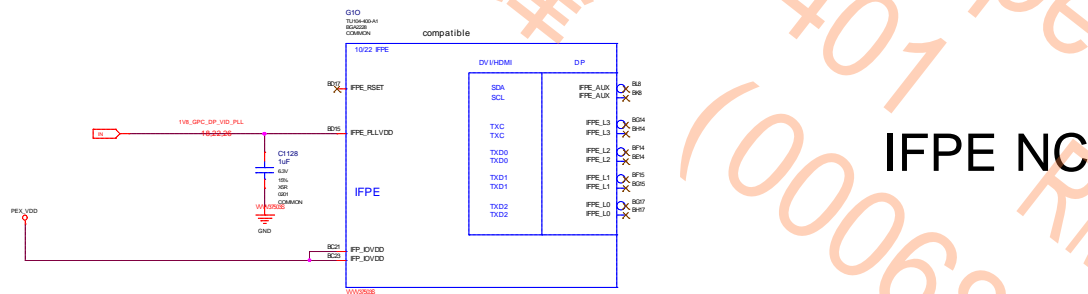
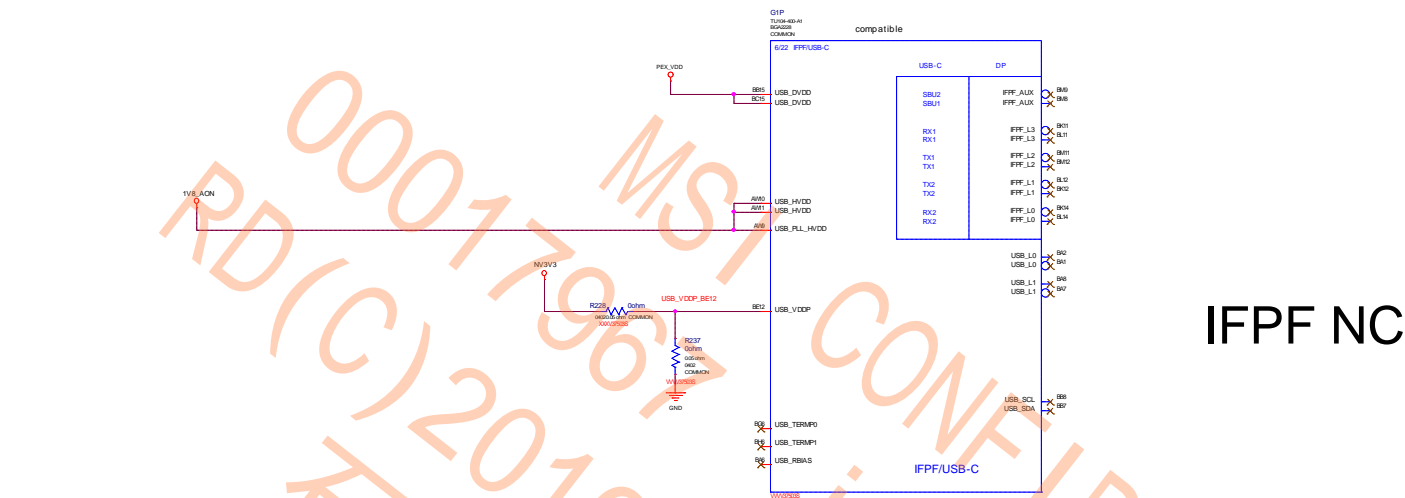


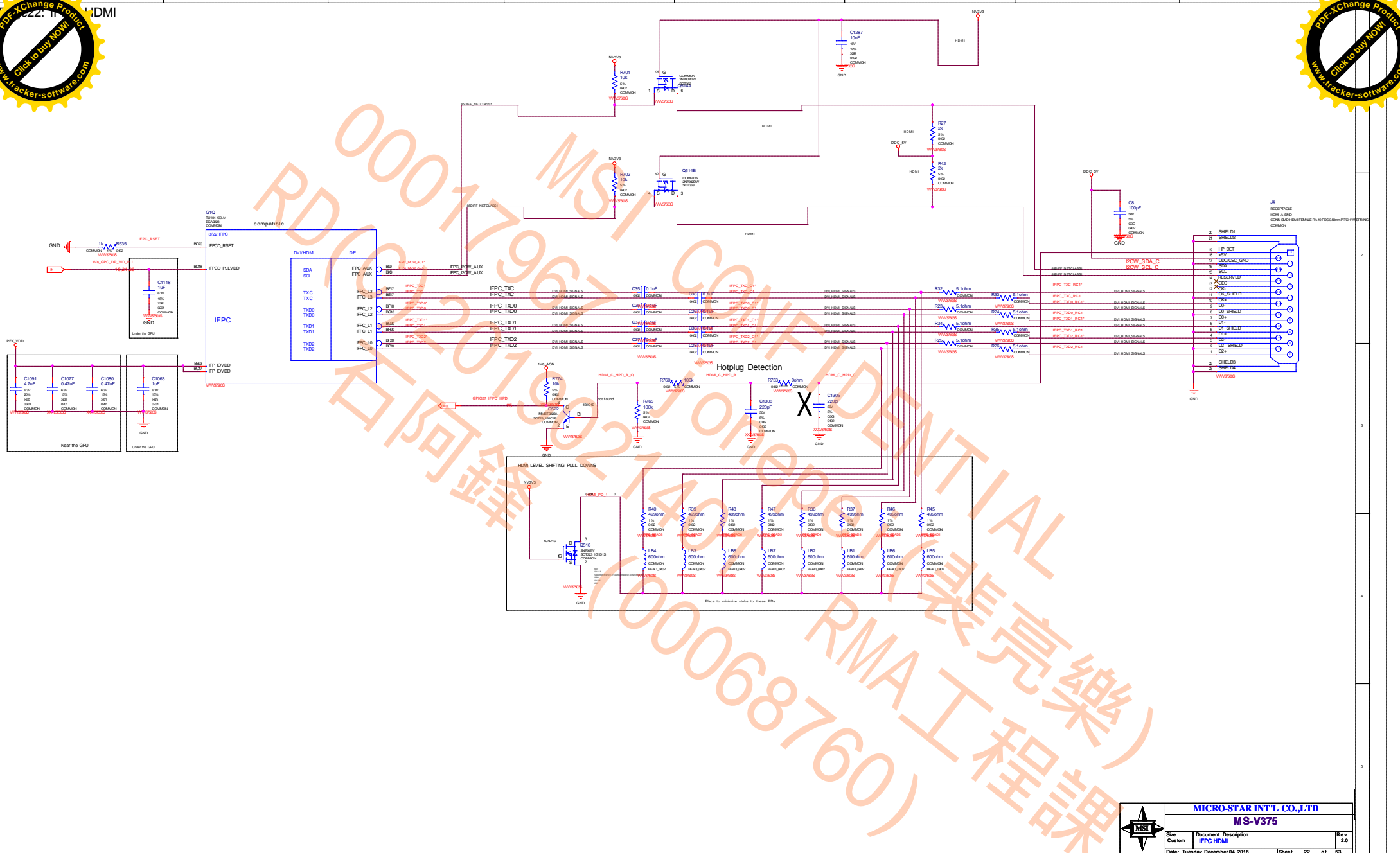
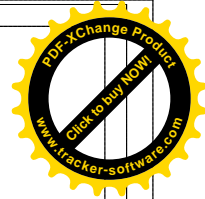
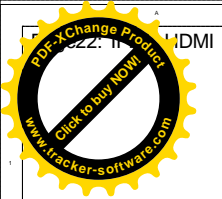
Fused DP_PWR



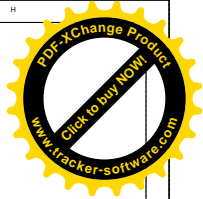


& IFPF USB-C NC






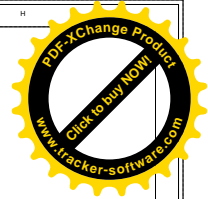
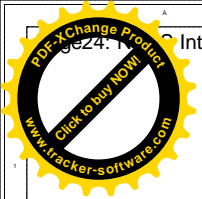
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MS-V375		
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Fused DP_PWR

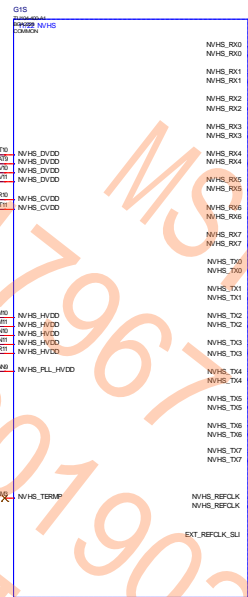
The diagram illustrates a fused DP_PWR circuit. It features a power supply input (VDDPWR) connected through a 0.1uF capacitor (C3) to a 1% resistor (R16). The output of R16 is connected to the IN pin of a 10A DP switch (U1). The switch is controlled by a 0.1uF capacitor (C4) connected to a 1% resistor (R17). The switch's output is connected to a 0.1uF capacitor (C10) and then to a 1% resistor (R18). The output of R18 is connected to the DP_PWR pin of a connector (C24). The switch is also connected to a 0.1uF capacitor (C11) connected to a 1% resistor (R19). The output of R19 is connected to the DP_PWR pin of a connector (C24). The switch is also connected to a 0.1uF capacitor (C12) connected to a 1% resistor (R20). The output of R20 is connected to the DP_PWR pin of a connector (C24).

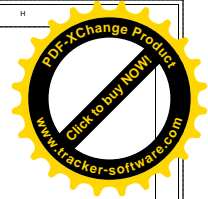
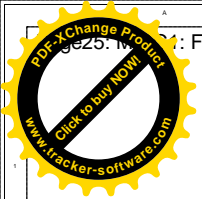
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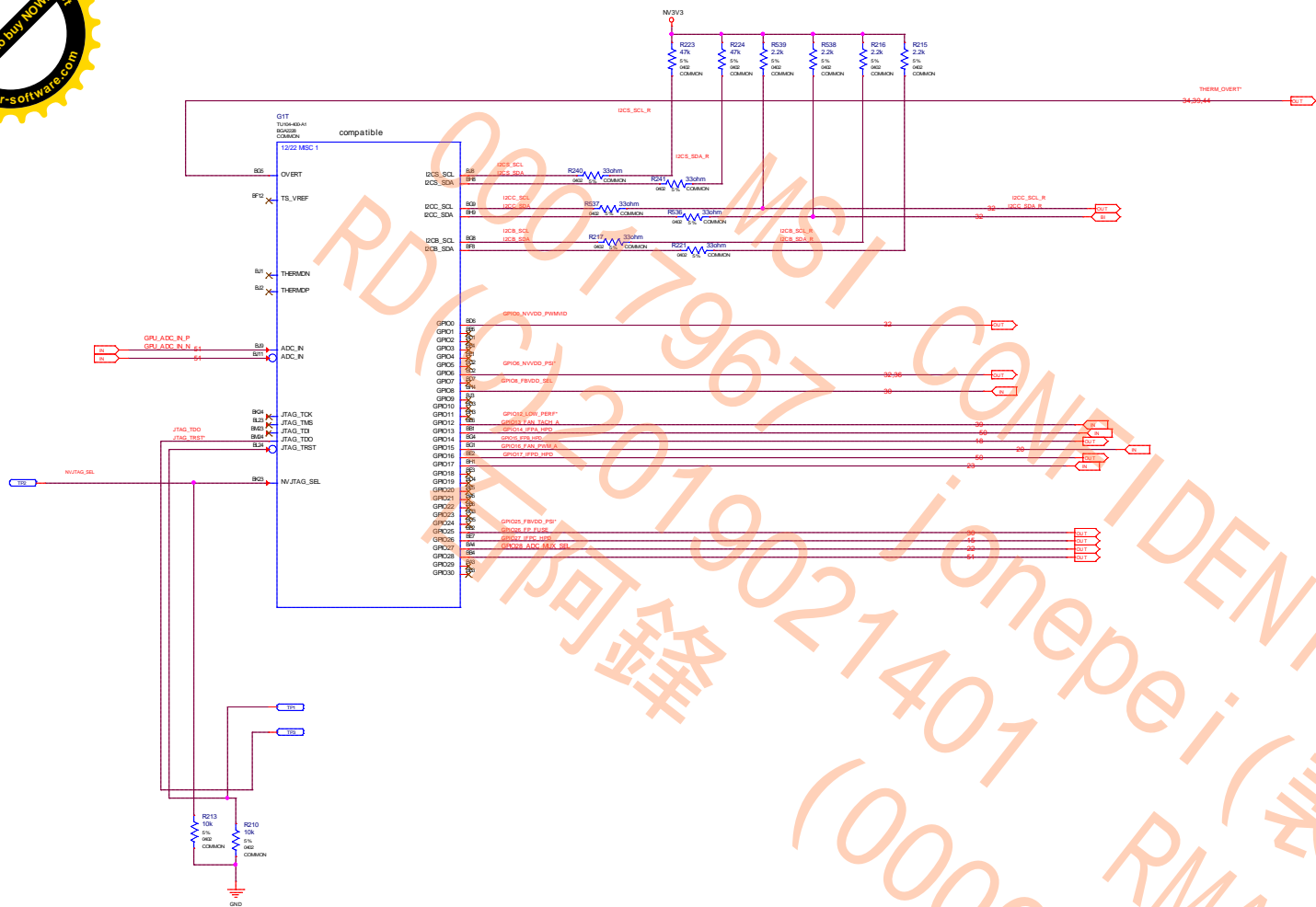
Interface and Frame Lock

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MS-V375: Fan, Thermal, JTAG, GPIO

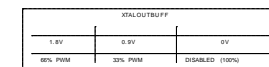
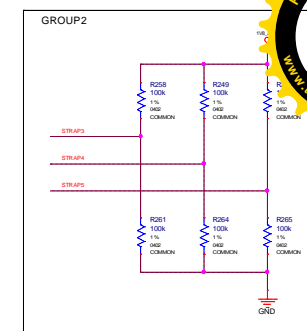


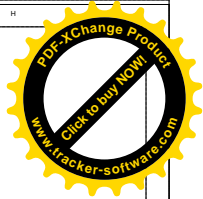
F-XChange Pr
e26: IV

RAMCFG[4:0]	DENSITY	WIDTH	VENDOR	DIE
00000	8Gb	256-bit	Samsung	C
00001	8Gb	256-bit	Micron	A
00010	8Gb	256-bit	Hynix	M
00011	8Gb	256-bit	Samsung	C
00100	8Gb	256-bit	Micron	A
00101	8Gb	256-bit	Hynix	M
00110	16Gb	256-bit	Samsung	M
00111				

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

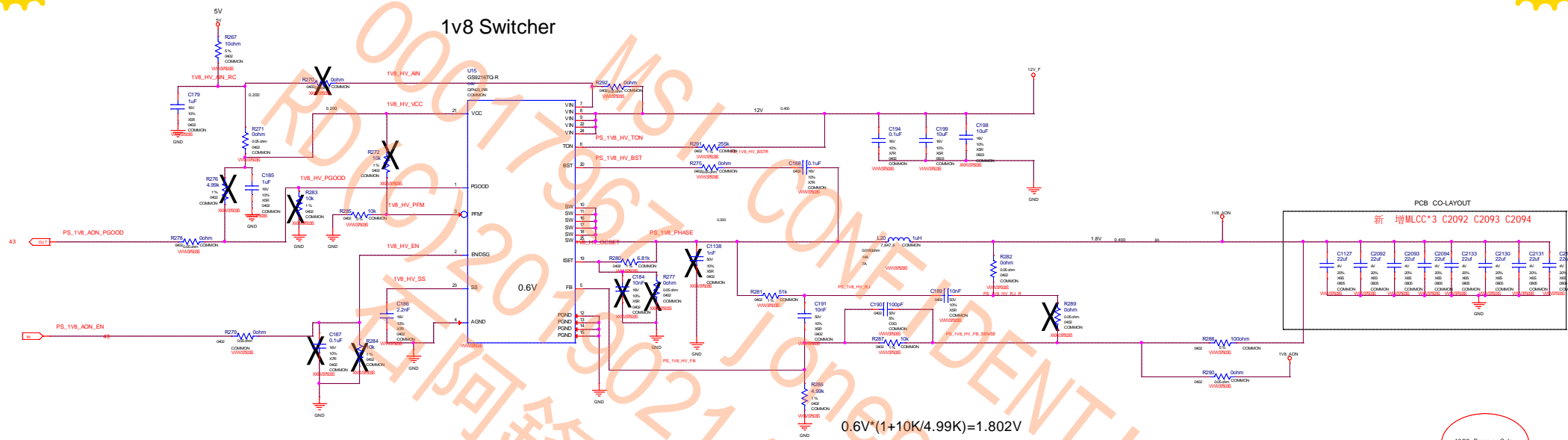
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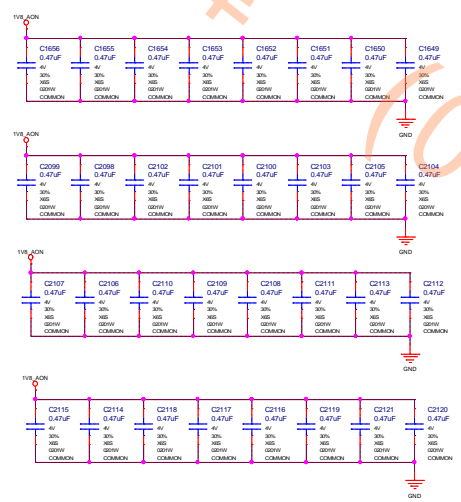
8_AON

1v8 Switcher

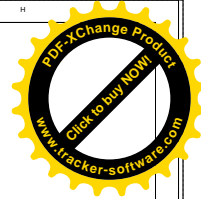


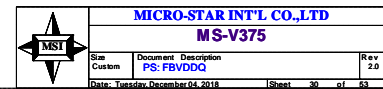
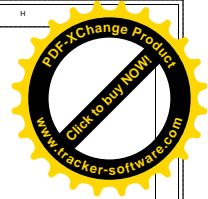
$$0.6V \cdot (1 + 10K/4.99K) = 1.802V$$

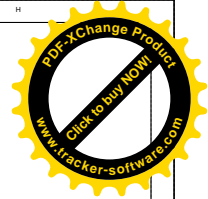
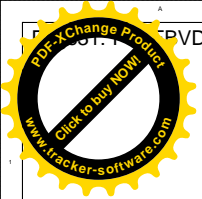
10/26 Remove Colay



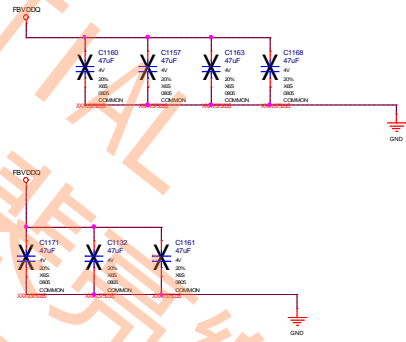
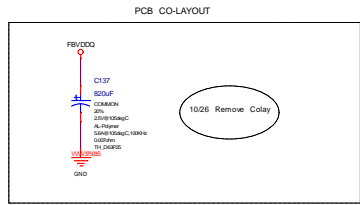
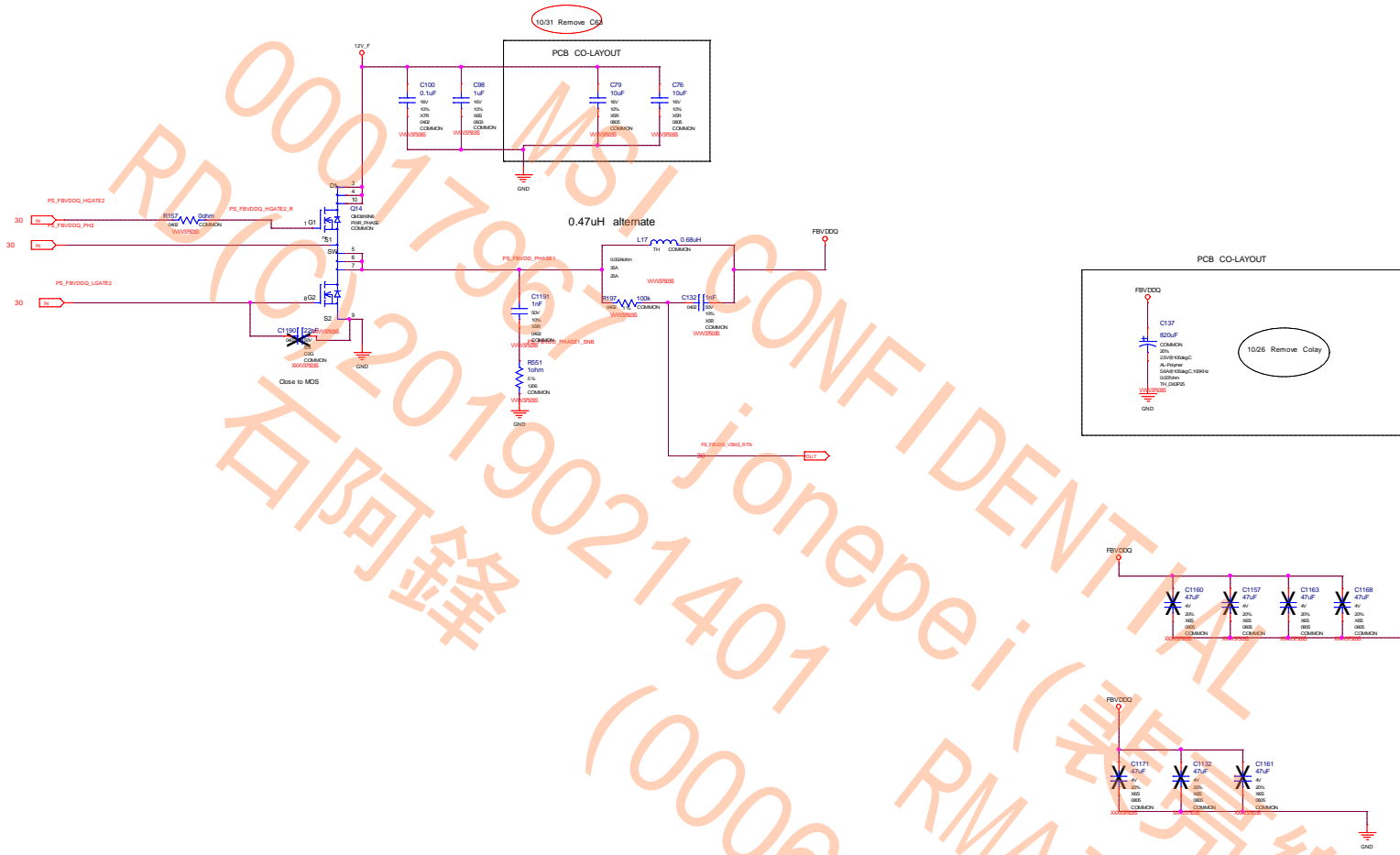
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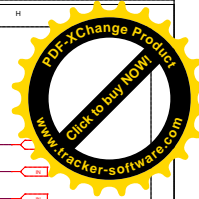


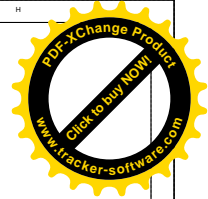
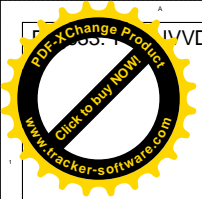


PSVDDQ PH2



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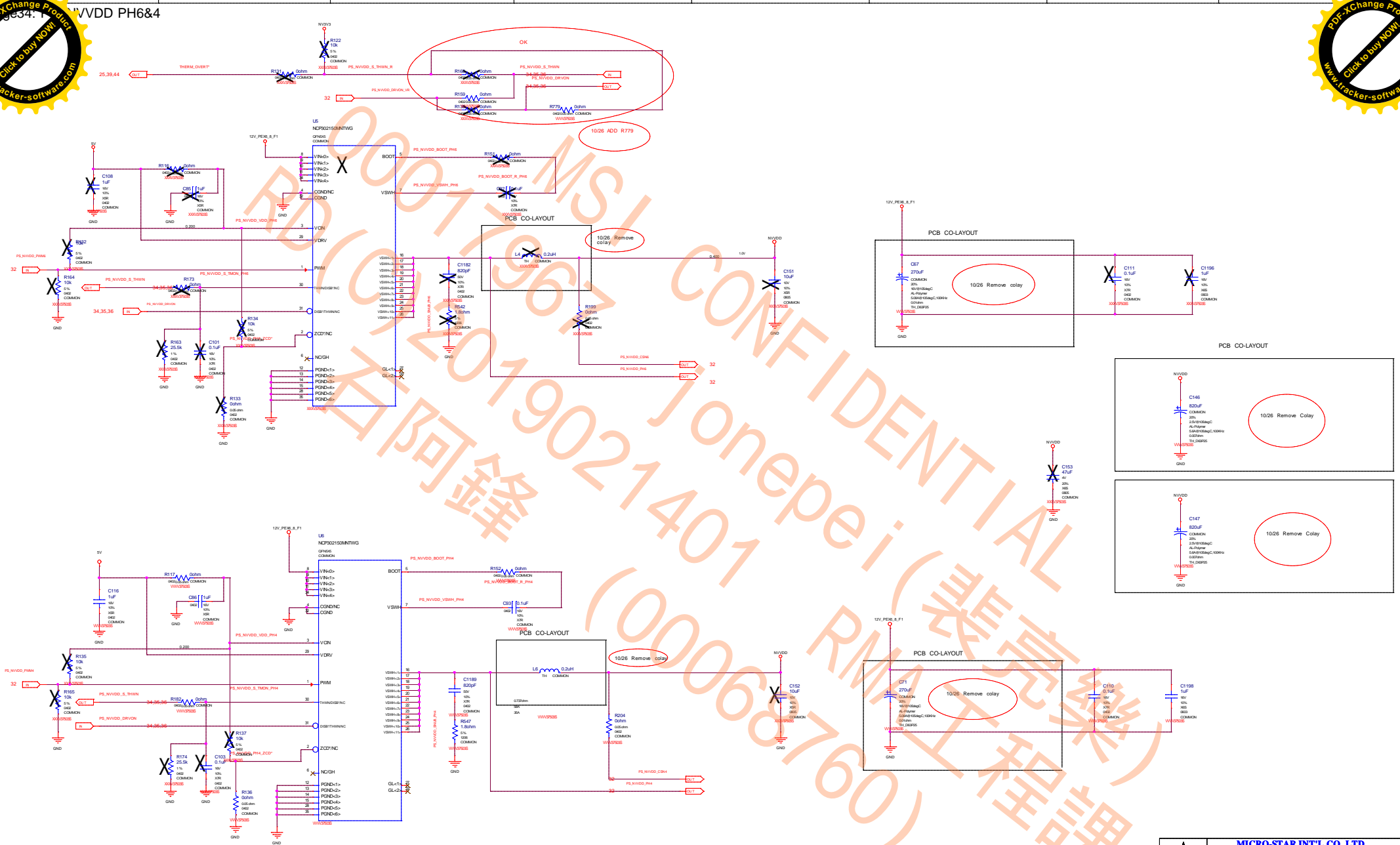
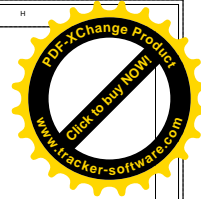


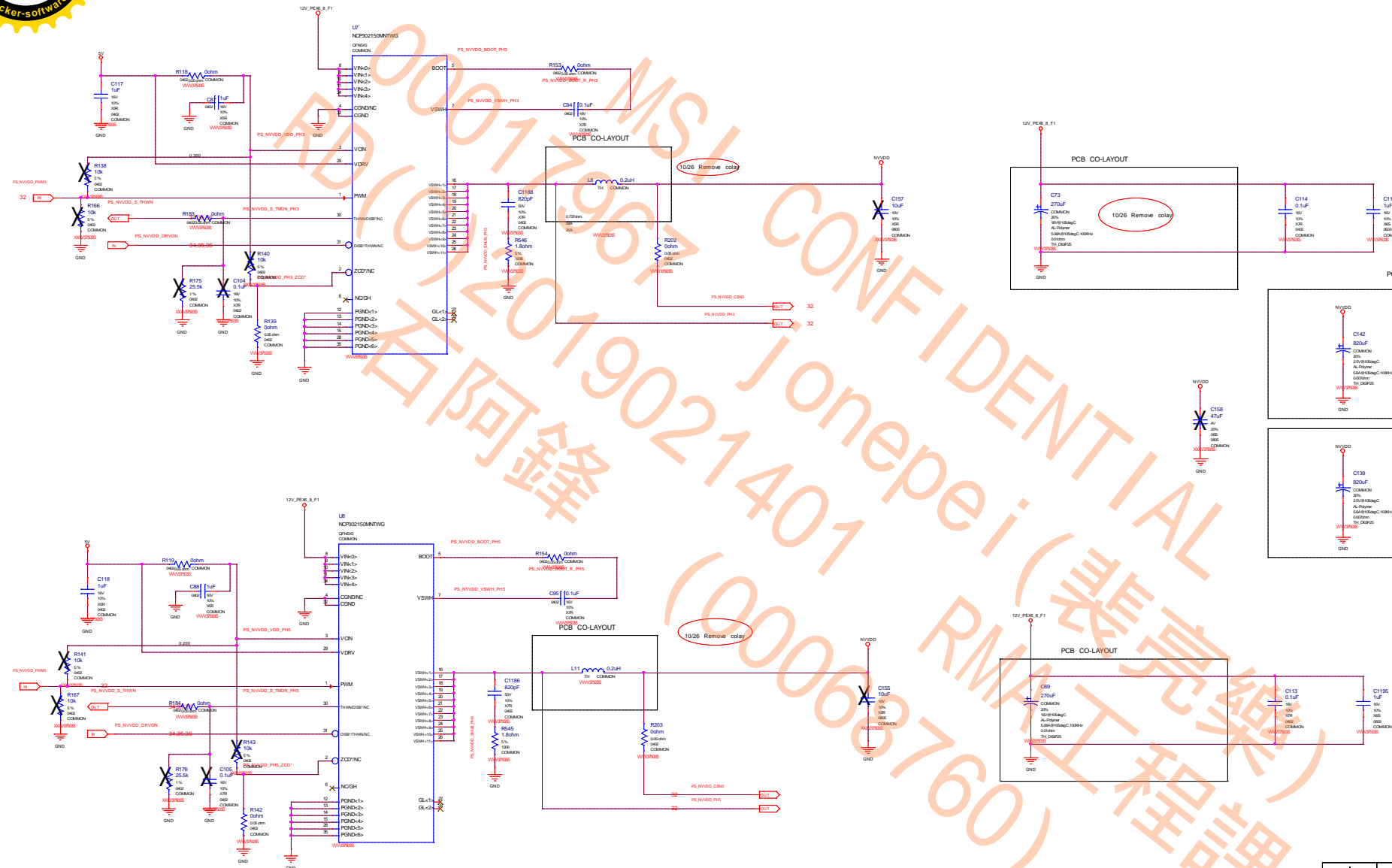
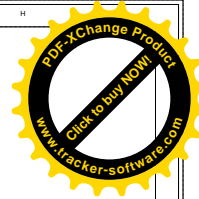


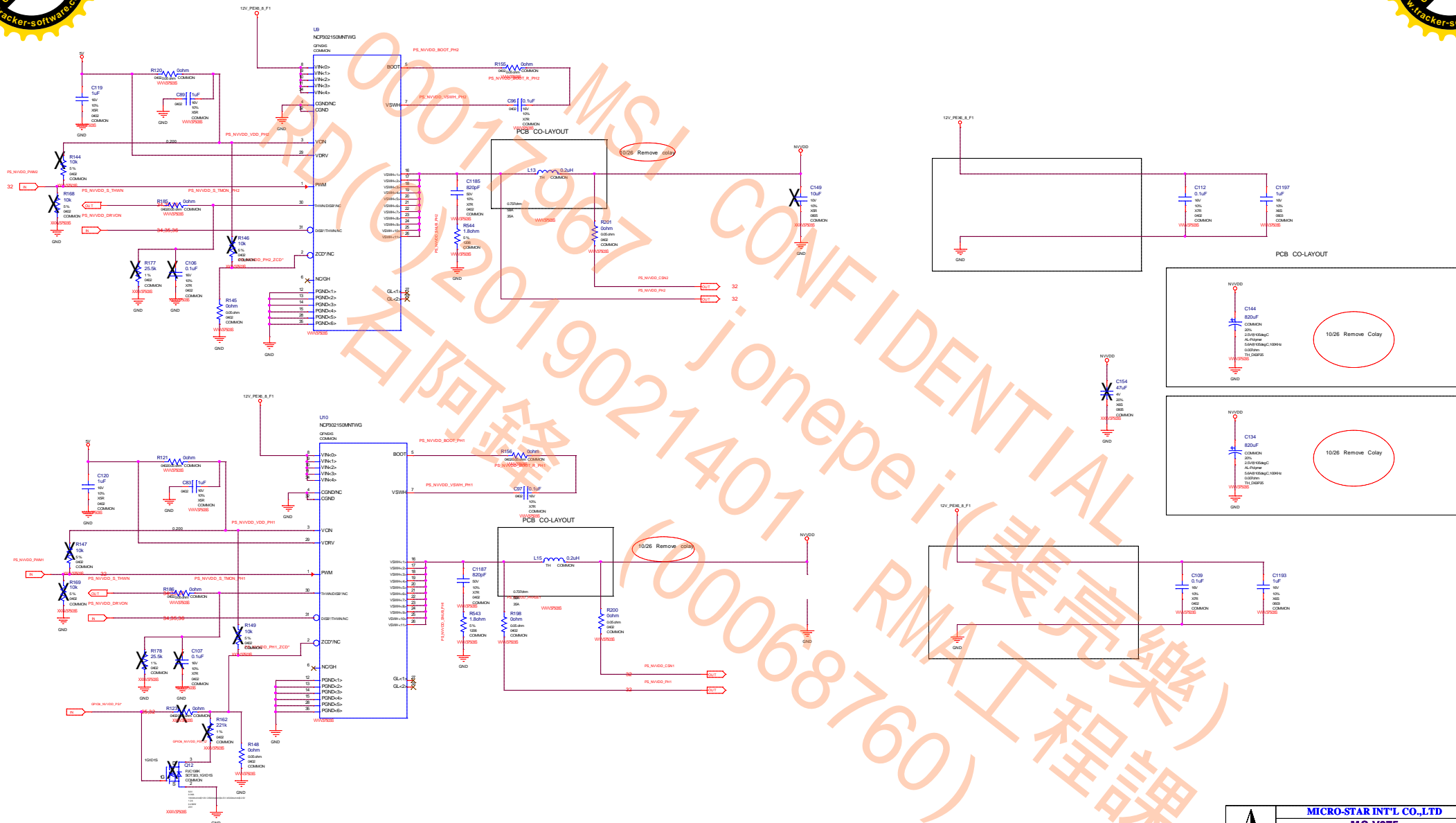
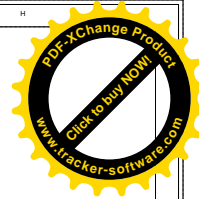
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00017967
RD(C)2019021401
石阿鋒
00068760
RMA工程課
jionepei (裴亮樂)

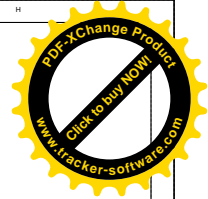
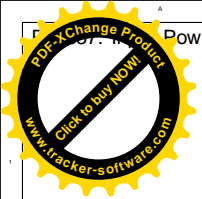
DROP OVR3i SOLUTION

MICRO-STAR INT'L CO.,LTD			
MS-V375			
Size	Document	Description	Rev
Custom	PS: NVDD CTR OVR3i		2.0
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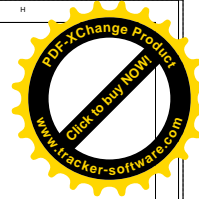




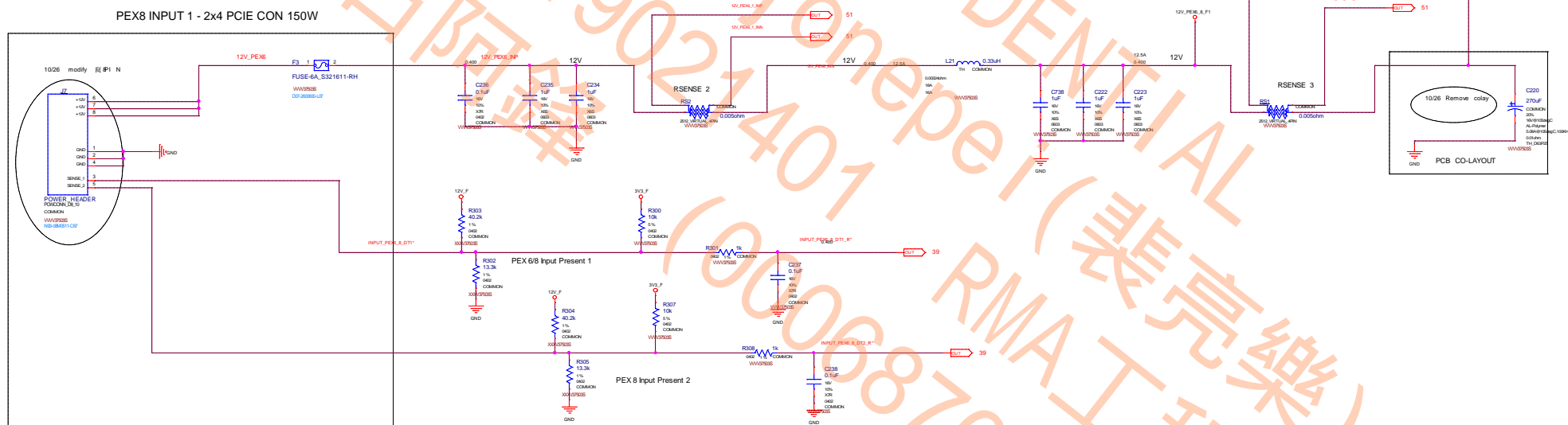
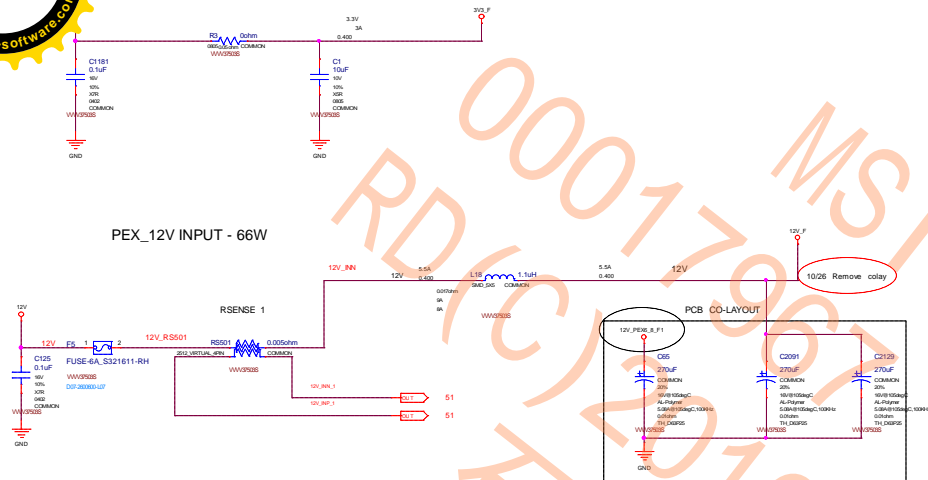
DELETE RAIL BALANCE CIRCUIT

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Custom		Input Power Balancing Switcher	2.0
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EX 3V3 INPUT - 10W



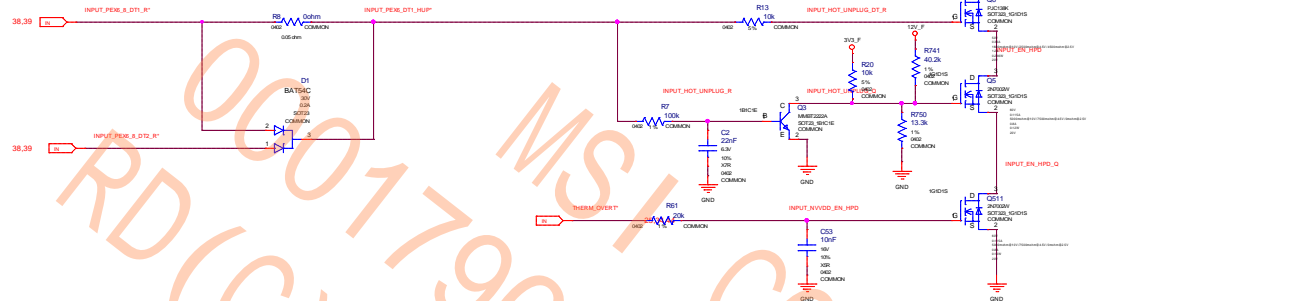
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Size Custom	Document Description PS: Inputs, Filtering, and Monitoring	Rev 2.0
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HOT-UNPLUG, UNDER POWER BOOT, CURRENT STEERING

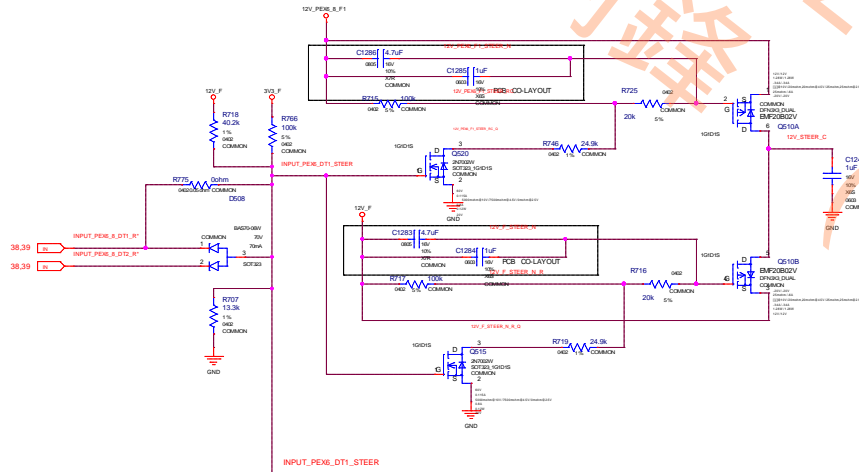
PEX Input Present 1

POWER CONNECTOR HOT-UNPLUG DETECT

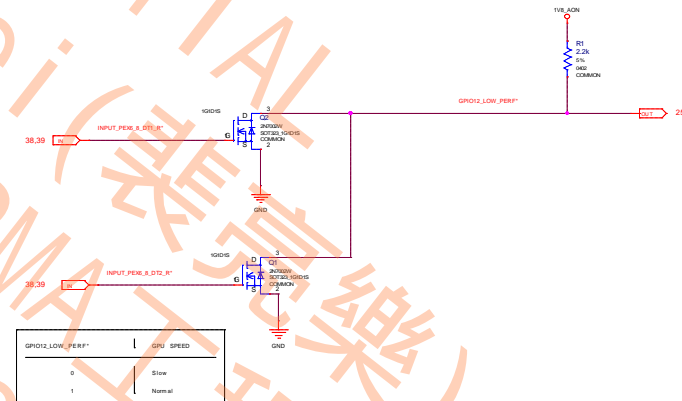


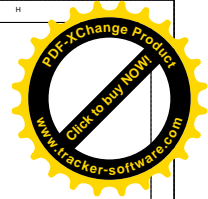
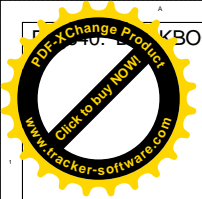
12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



PEX Input - Power Level/PSI* Control





BUCKBOOST

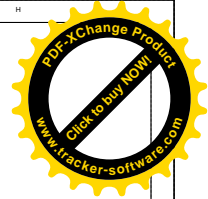
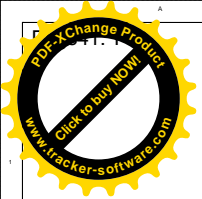
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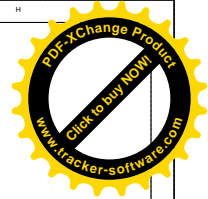
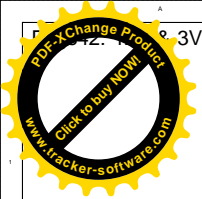
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	Size	Document Description	Rev
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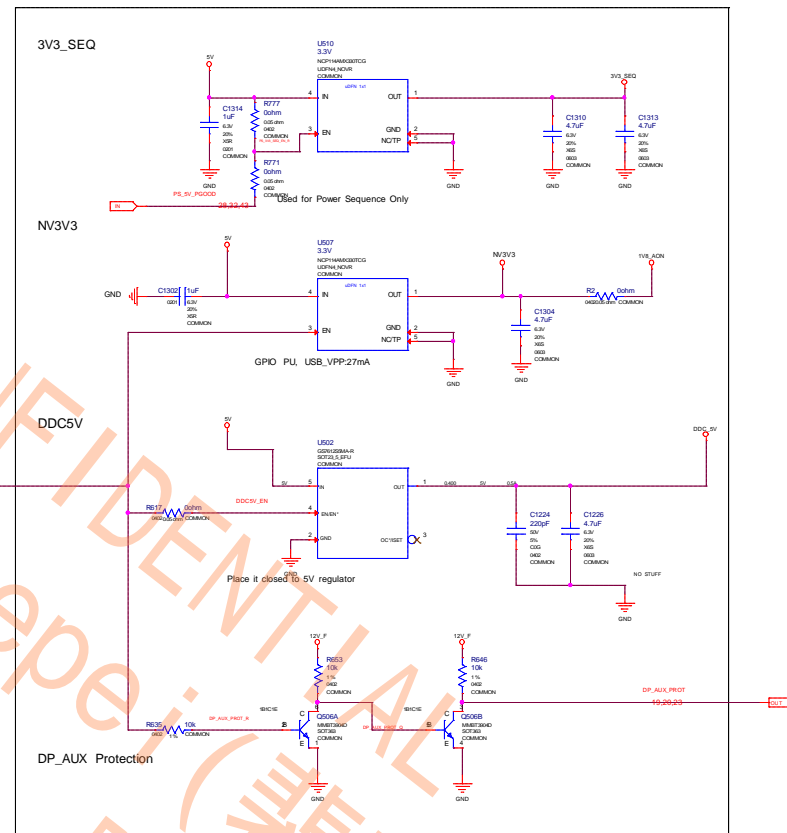
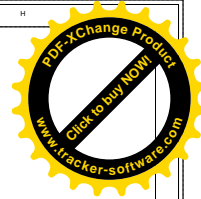
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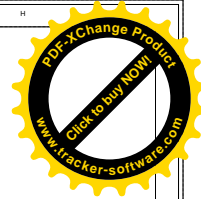
	MICRO-STAR INT'L CO.,LTD		
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	Custom	PPC	2.0
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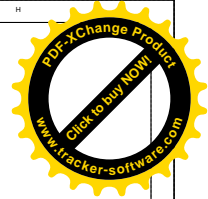
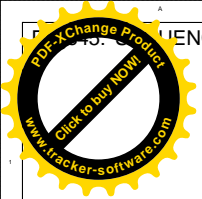
MICRO-STAR INT'L CO.,LTD			
MS-V375			
Size	Document	Description	Rev
Custom	12V & 3V3_A	SWITCHER	2.0
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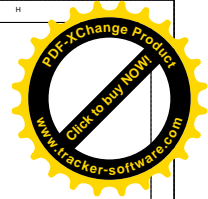
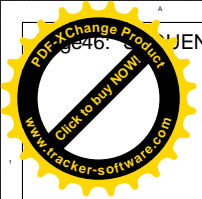
25,34





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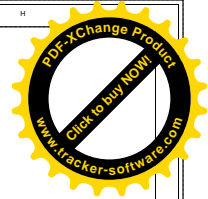
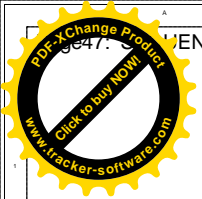
MICRO-STAR INT'L CO.,LTD		
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Custom	SEQUENCE:Voltage Monitor	2.0
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CONFIDENTIAL: Discharge

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ENCE:MISC

PEX_CLKREQ*



PU ON GPU SIDE

NC ON GOLD FINGER SIDE

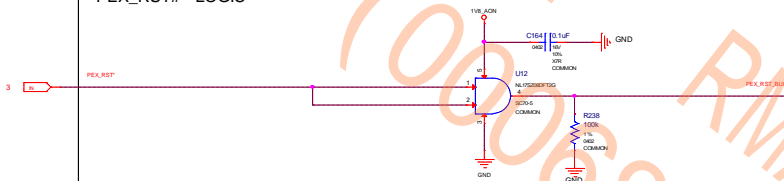
GPU_WAKE*



PU ON GPU SIDE

NC ON GOLD FINGER SIDE

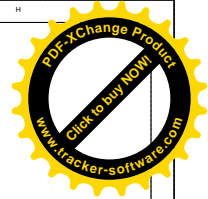
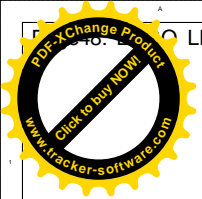
PEX_RST# LOGIC



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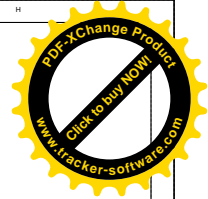
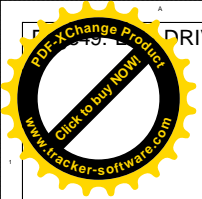
Size	Document Description	Rev
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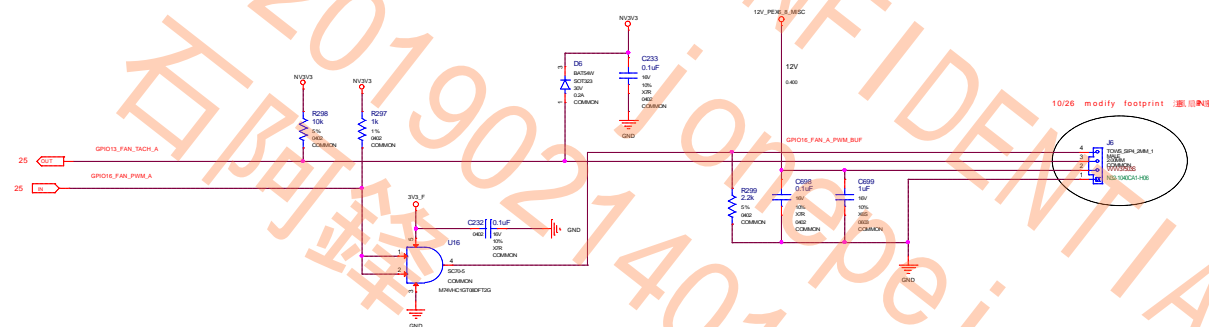
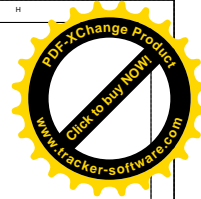


DRIVER & Boost



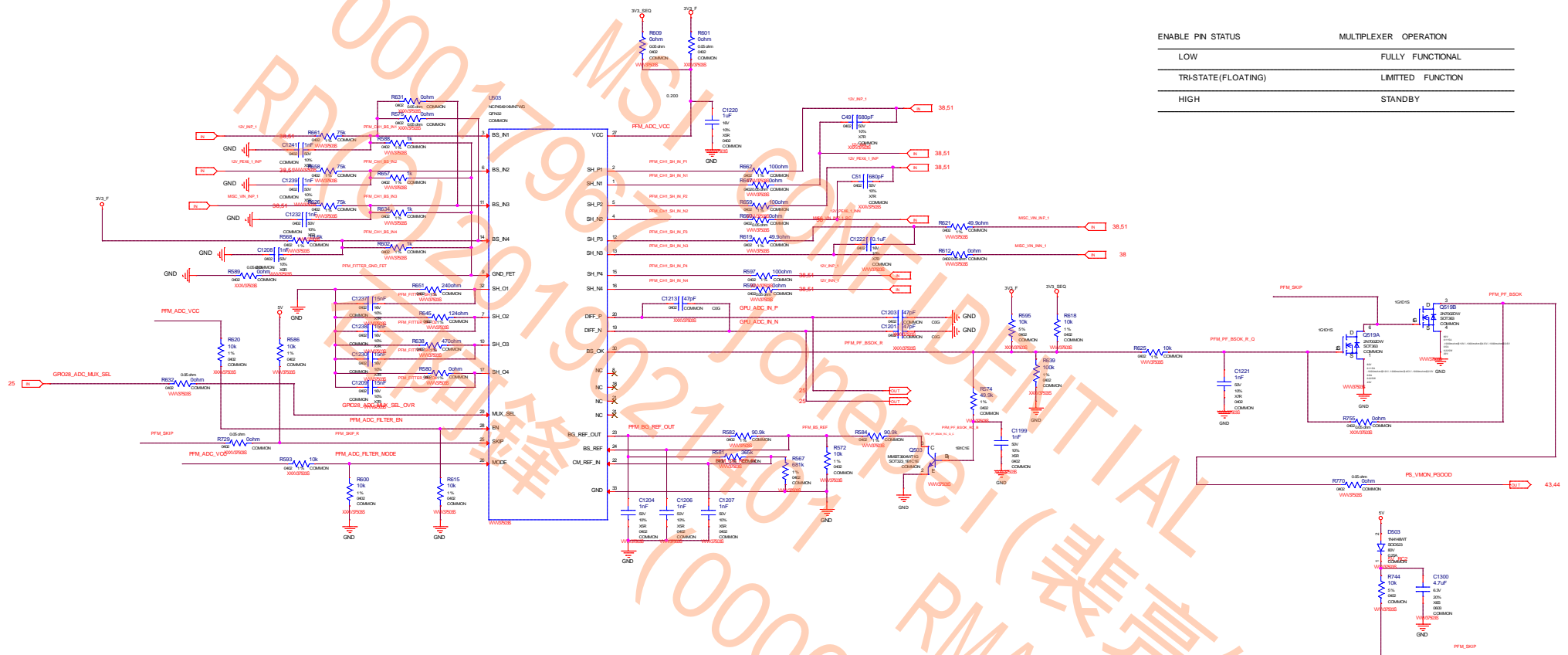
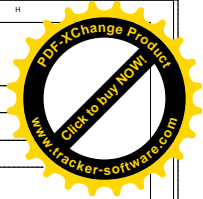
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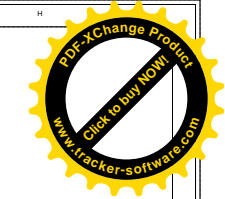
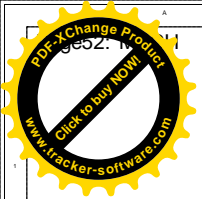
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Size Custom	Document Description FAN	Re 2
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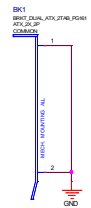
MICRO-STAR INT'L CO.,LTD
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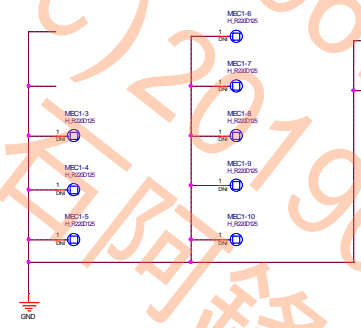


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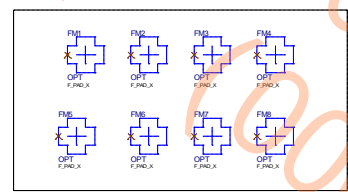
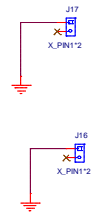
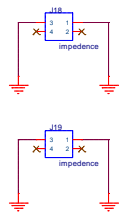
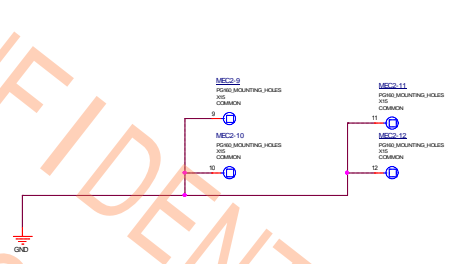
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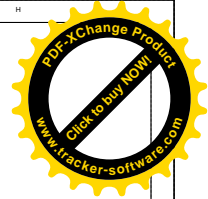
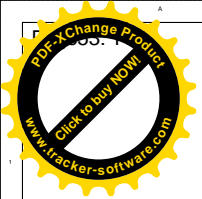
MOUNTING HOLES FOR HS:




BACK STIFFENER:



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	Size	Document	Description	Rev
	Custom	PTC		2.0
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